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Class	Subclass	
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. *3*
 PATENT DATE
 SCANNED *LMZ* Q.A. *SM*

APPLICATION NO. 09/932781	CONT/PRIOR F	CLASS 711	SUBCLASS 1244	ART UNIT 2185	EXAMINER Bradson M. Kim
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APPLICANTS

Serge Lasserre

अनुसूची

- Local memory with indicator bits to support concurrent DMA and CPU access

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drawg.	Figs. Drawg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due			Date Paid	
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